

**Amendments to the Specification:**

Please replace paragraph [0011] with the following amended paragraph:

[0011] In the claimed invention, an MVA LCD panel is disclosed. The MVA LCD panel includes a plurality of data lines, a top substrate, and a bottom substrate in parallel with  
5 each other. The bottom substrate includes a plurality of pixel regions thereon. The MVA LCD panel further includes a plurality of electrode patterns ~~common lines~~ on a surface of the bottom substrate facing the top substrate. Each electrode pattern further comprises a first electrode pattern parallel to the data line and two second electrode patterns perpendicular to the first electrode pattern. The MVA LCD panel further includes a  
10 plurality of pixel electrode respectively positioned in each pixel region and above the ~~common lines~~ electrode patterns, a liquid crystal layer filled between the top substrate and the bottom substrate, a common electrode layer positioned on a surface of the top substrate facing the bottom substrate, and a plurality of protrusions on a surface of the common electrode layer. ~~Each common line~~ Each electrode pattern passes through  
15 corresponding pixel regions, and each pixel electrode includes a plurality of slits. In addition, each protrusion is arranged parallel to and alternatively with each slit, and the protrusions ~~partially overlap the common lines~~ in the pixel region are positioned above the first electrode pattern in each pixel region.

20 Please replace paragraph [0024] with the following amended paragraph:

[0024] As described, the liquid crystal molecules close to the protrusions 78 are slightly inclined while no voltage is applied, which leads to light leakage problems. Consequently, the MVA LCD panel 50 includes a plurality of common lines (electrode patterns) 82  
25 positioned on the bottom substrate 51 in the pixel region 60. Since the common lines 82 and the protrusions 78 in the pixel region 60 are partially overlapped, the light leakage in the edge of the protrusions 78 is covered. In addition, the dimensions of the common lines 82 are adjustable in accordance with practical effect. It is worth noting that the common

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lines 82 also function as electrodes of storage capacitors, and are simultaneously formed on the bottom substrate 51 with the gate 62 and all scan lines (not shown), thus the complexity of processes is not increased. Furthermore, the common lines 82 can be electrically connected together according to different circuit design considerations for  
5 improving the effect of the storage capacitor.